

FIG. 1

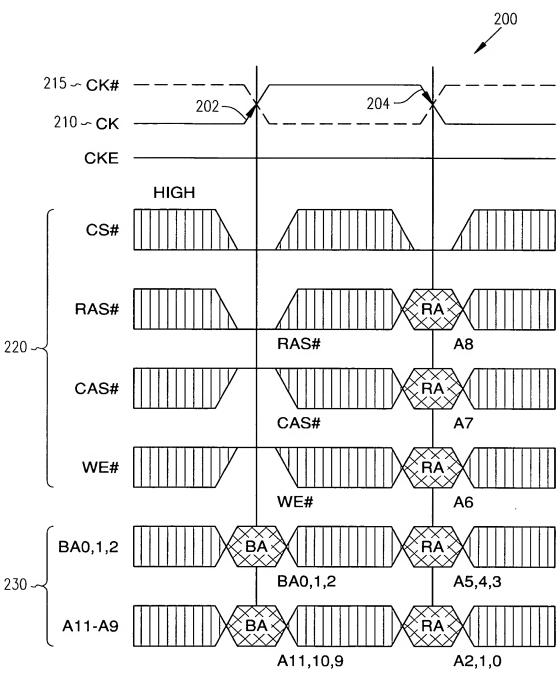


FIG. 2

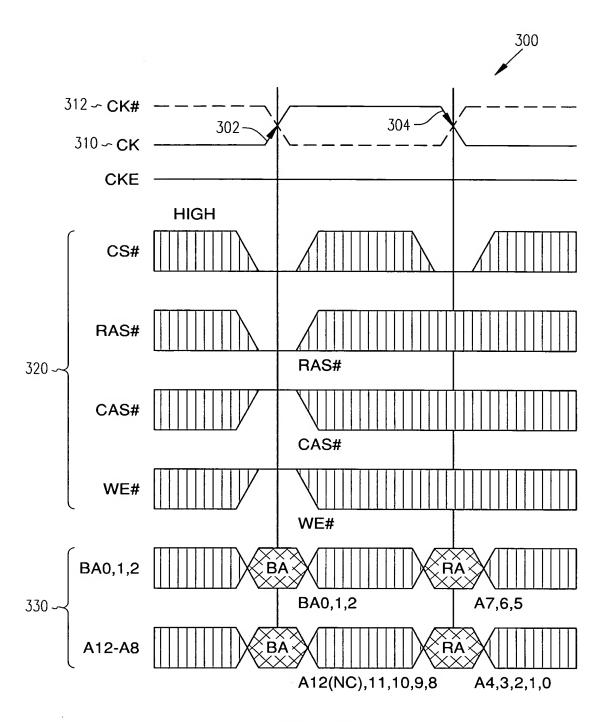


FIG. 3

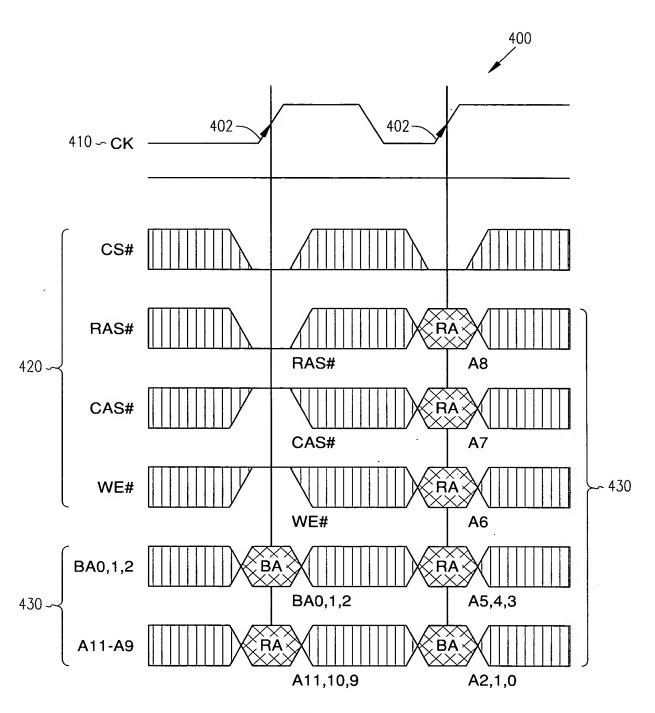


FIG. 4

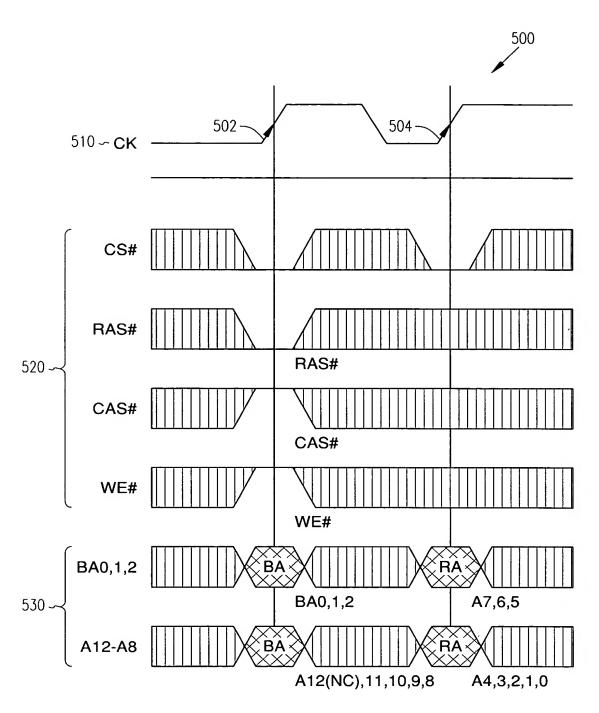


FIG. 5

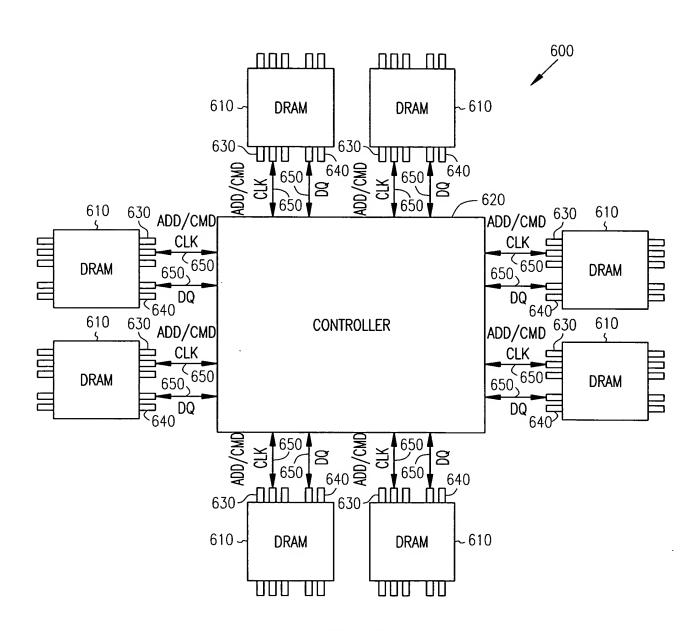


FIG. 6

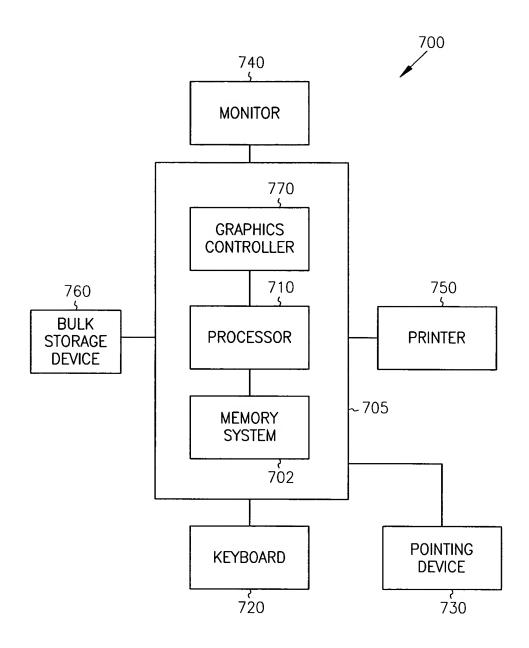
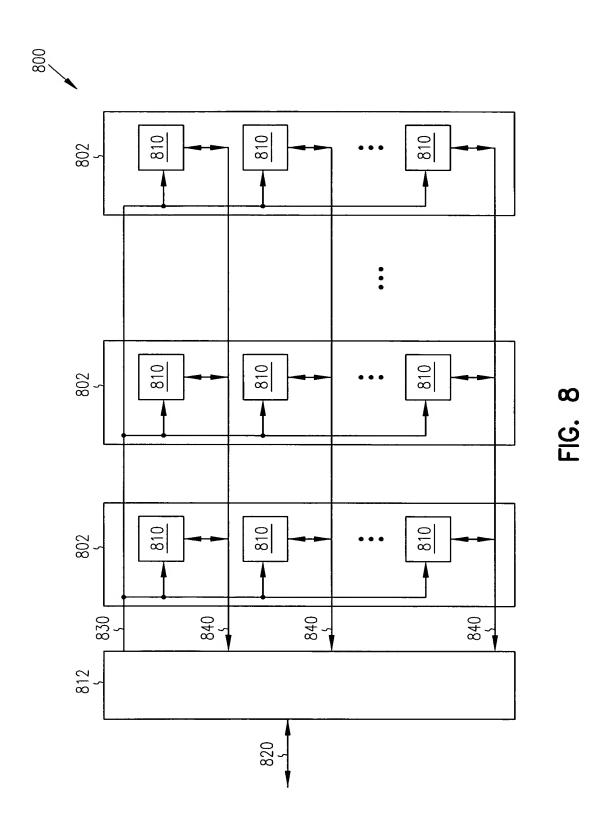


FIG. 7

TITLE: DUAL EDGE COMMAND IN DRAM INVENTORS NAME: Joo S. Choi et al. DOCKET NO.: 303.873US1



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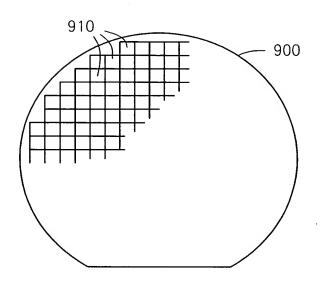


FIG. 9